

Notice of References Cited	Application/Control No. 10/687,608	Applicant(s)/Patent Under Reexamination CHIDAMBARRAO ET AL.	
	Examiner Stanetta D. Isaac	Art Unit 2812	Page 1 of 1

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NON-PATENT DOCUMENTS

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	U	Stanley Wolf and Richard N. Tauber, Silicon Processing For The VLSI Era, 2000, Lattice Press, Second Edition, 256-257
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.